FIGURE 1.

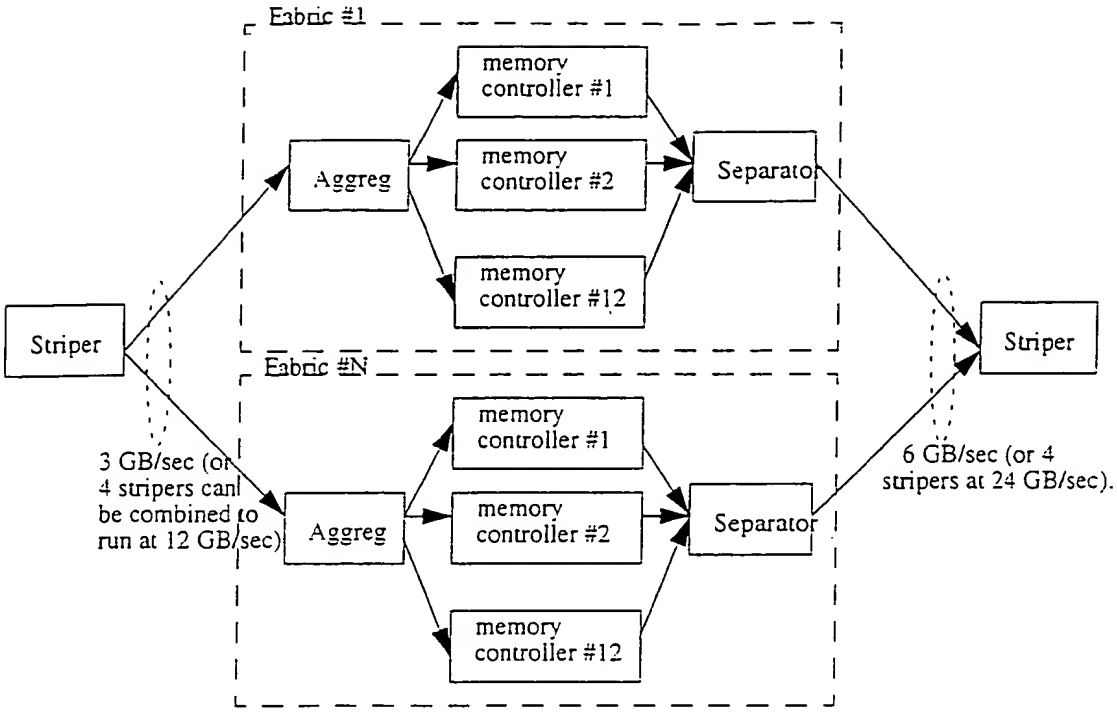

Fabric #1
memory controller #1
memory controller #2
memory controller #12
Aggreg
Separator
Striper
Striper
Fabric #N
memory controller #1
memory controller #2
memory controller #12
Aggreg
Separator
3 GB/sec (or 4 stripers can be combined to run at 12 GB/sec)
6 GB/sec (or 4 stripers at 24 GB/sec).

FIGURE 2.

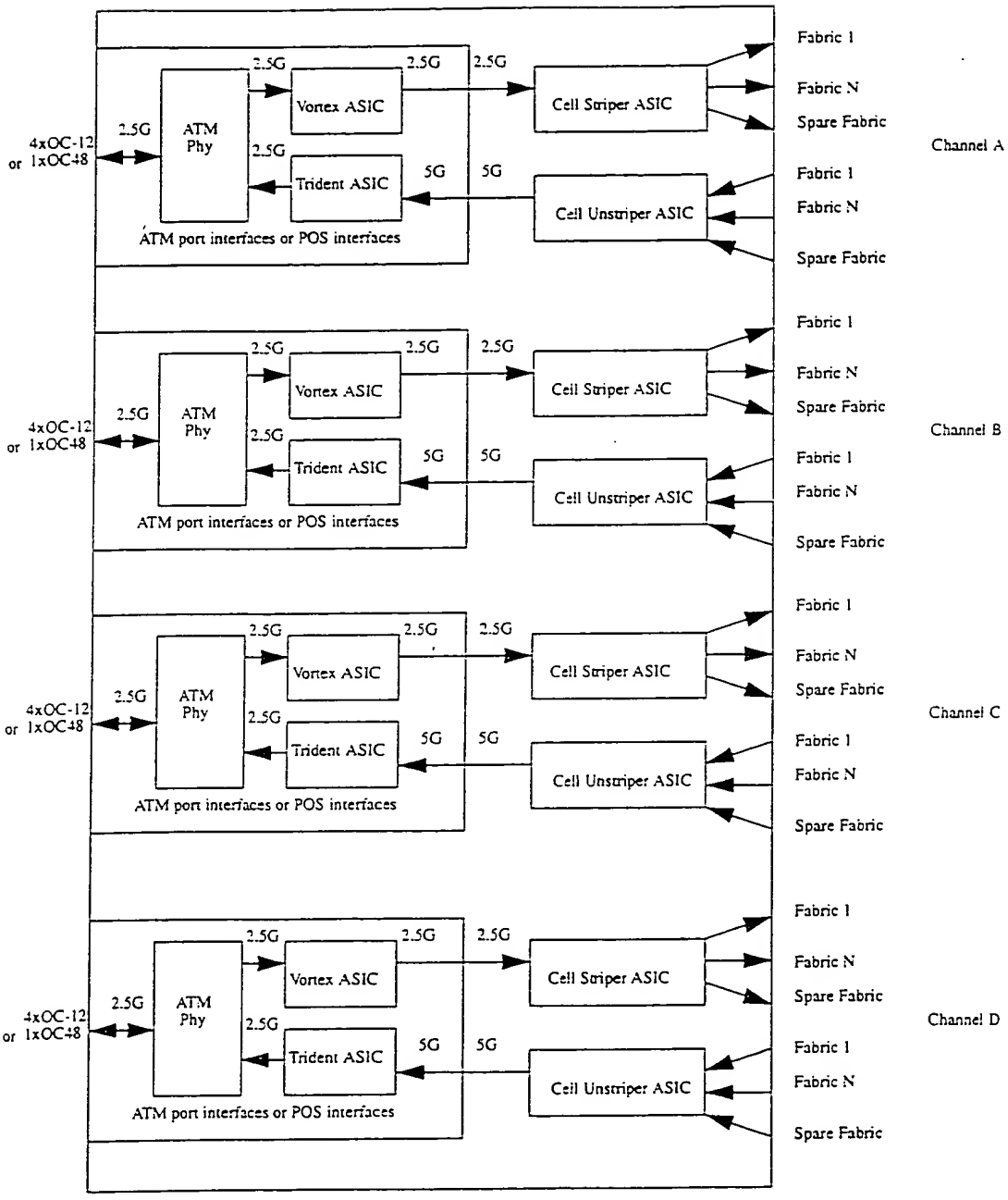

4xOC-12
or 1xOC48
2.5G
ATM
Phy
2.5G
Vortex ASIC
2.5G
2.5G
Cell Striper ASIC
2.5G
2.5G
Trident ASIC
5G
5G
Cell Unstriper ASIC
ATM port interfaces or POS interfaces
Fabric 1
Fabric N
Spare Fabric
Channel A
Channel B
Channel C
Channel D

FIGURE 3.

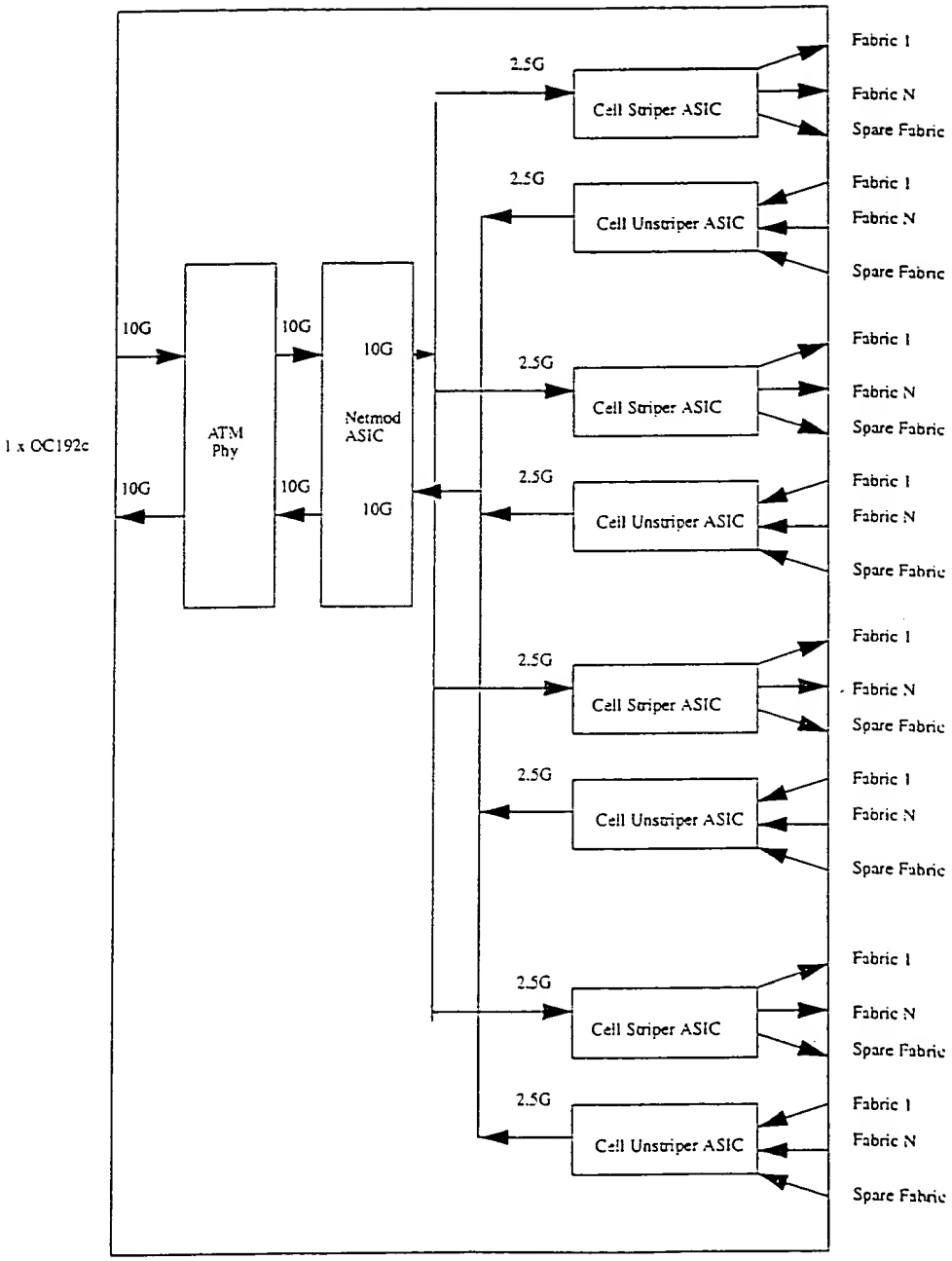

1 x OC192c
10G
ATM Phy
10G
10G
Netmod ASIC
10G
10G
10G
2.5G
Cell Striper ASIC
Fabric 1
Fabric N
Spare Fabric
2.5G
Cell Unstriper ASIC
Fabric 1
Fabric N
Spare Fabric
2.5G
Cell Striper ASIC
Fabric 1
Fabric N
Spare Fabric
2.5G
Cell Unstriper ASIC
Fabric 1
Fabric N
Spare Fabric
2.5G
Cell Striper ASIC
Fabric 1
Fabric N
Spare Fabric
2.5G
Cell Unstriper ASIC
Fabric 1
Fabric N
Spare Fabric
2.5G
Cell Striper ASIC
Fabric 1
Fabric N
Spare Fabric
2.5G
Cell Unstriper ASIC
Fabric 1
Fabric N
Spare Fabric

FIGURE 4.

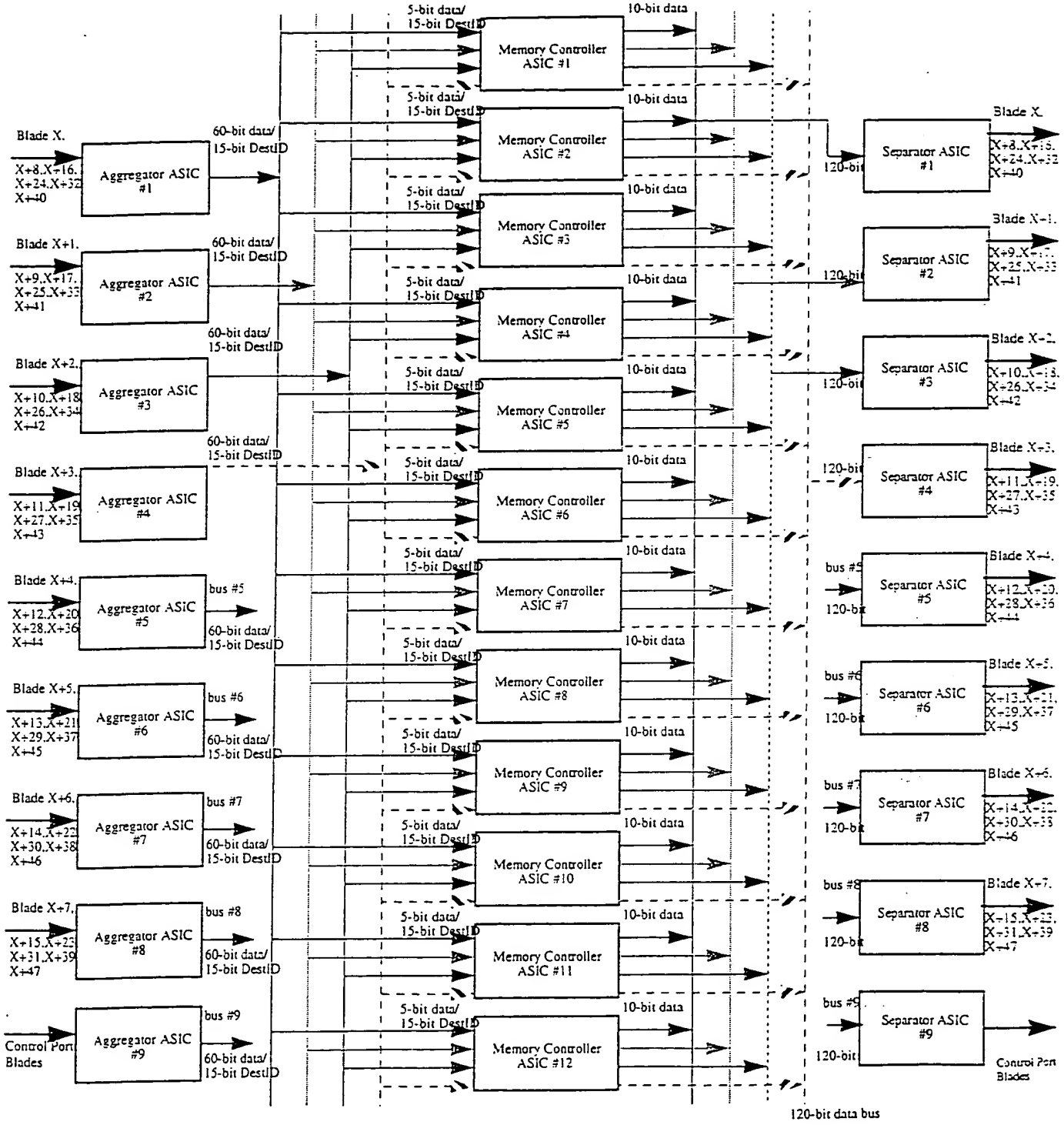

Blade X.
X+8,X+16,
X+24,X+32
X+40
Blade X+1.
X+9,X+17,
X+25,X+33
X+41
Blade X+2.
X+10,X+18,
X+26,X+34
X+42
Blade X+3.
X+11,X+19,
X+27,X+35
X+43
Blade X+4.
X+12,X+20,
X+28,X+36
X+44
Blade X+5.
X+13,X+21,
X+29,X+37
X+45
Blade X+6.
X+14,X+22,
X+30,X+38
X+46
Blade X+7.
X+15,X+23,
X+31,X+39
X+47
Control Port
Blades
Aggregator ASIC #1
Aggregator ASIC #2
Aggregator ASIC #3
Aggregator ASIC #4
Aggregator ASIC #5
Aggregator ASIC #6
Aggregator ASIC #7
Aggregator ASIC #8
Aggregator ASIC #9
60-bit data/
15-bit DestID
bus #5
bus #6
bus #7
bus #8
bus #9
5-bit data/
15-bit DestID
Memory Controller ASIC #1
Memory Controller ASIC #2
Memory Controller ASIC #3
Memory Controller ASIC #4
Memory Controller ASIC #5
Memory Controller ASIC #6
Memory Controller ASIC #7
Memory Controller ASIC #8
Memory Controller ASIC #9
Memory Controller ASIC #10
Memory Controller ASIC #11
Memory Controller ASIC #12
10-bit data
120-bit
Separator ASIC #1
Separator ASIC #2
Separator ASIC #3
Separator ASIC #4
Separator ASIC #5
Separator ASIC #6
Separator ASIC #7
Separator ASIC #8
Separator ASIC #9
Blade X.
Blade X+1.
Blade X+2.
Blade X+3.
Blade X+4.
Blade X+5.
Blade X+6.
Blade X+7.
Control Port
Blades
120-bit data bus

FIGURE 5.

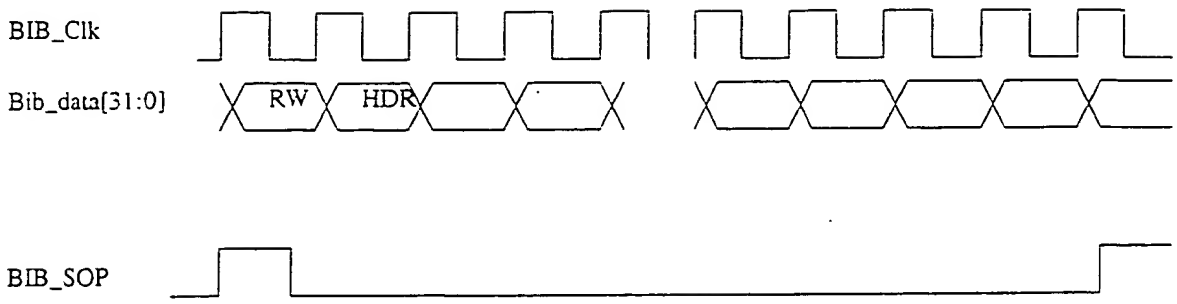


FIGURE 6.

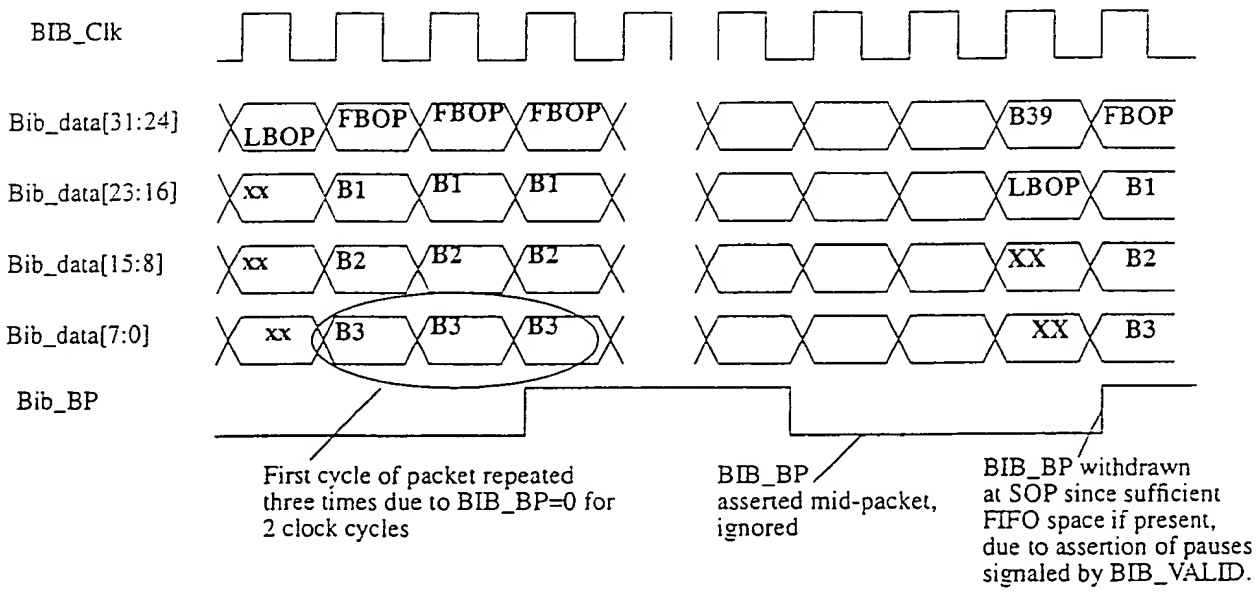
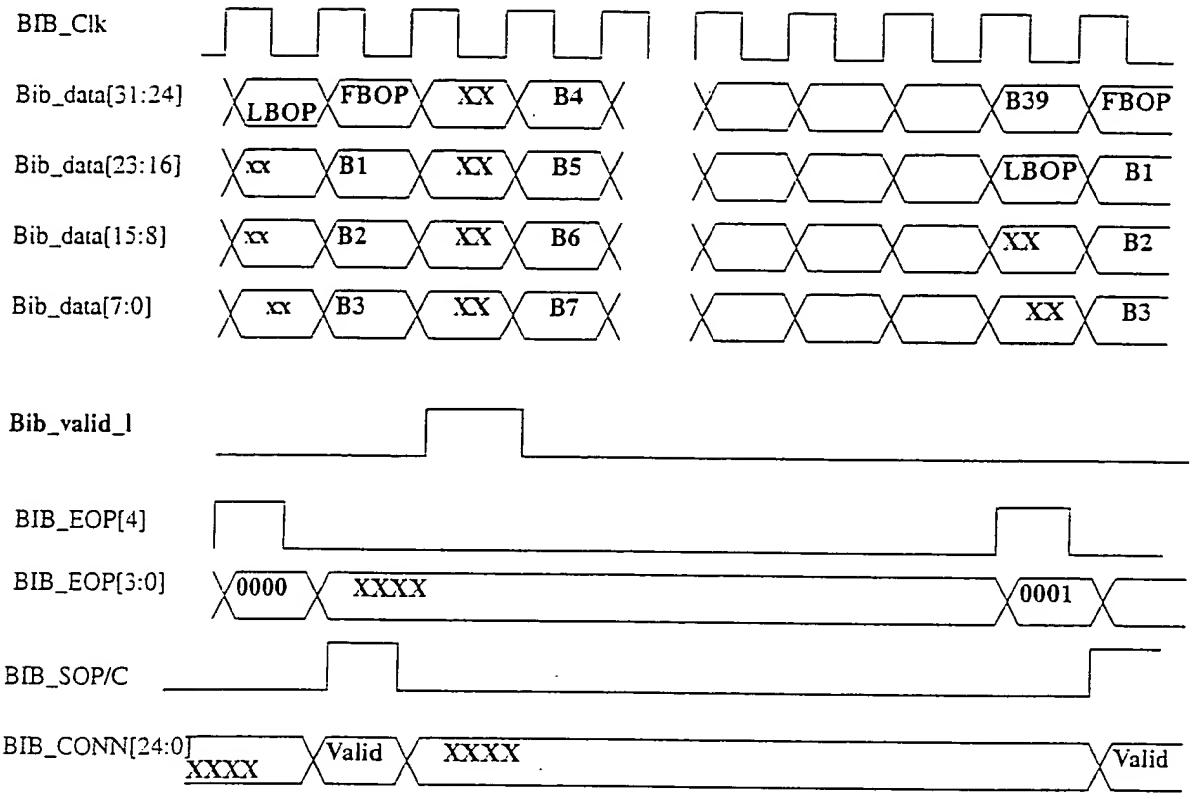


FIGURE 7.



Note: LBOP = Last Byte of Packet, FBOP = First byte of packet.

FIGURE 8.

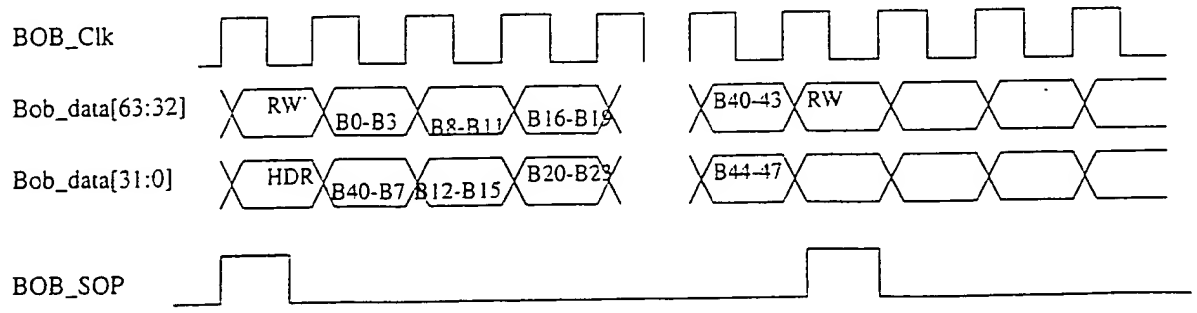
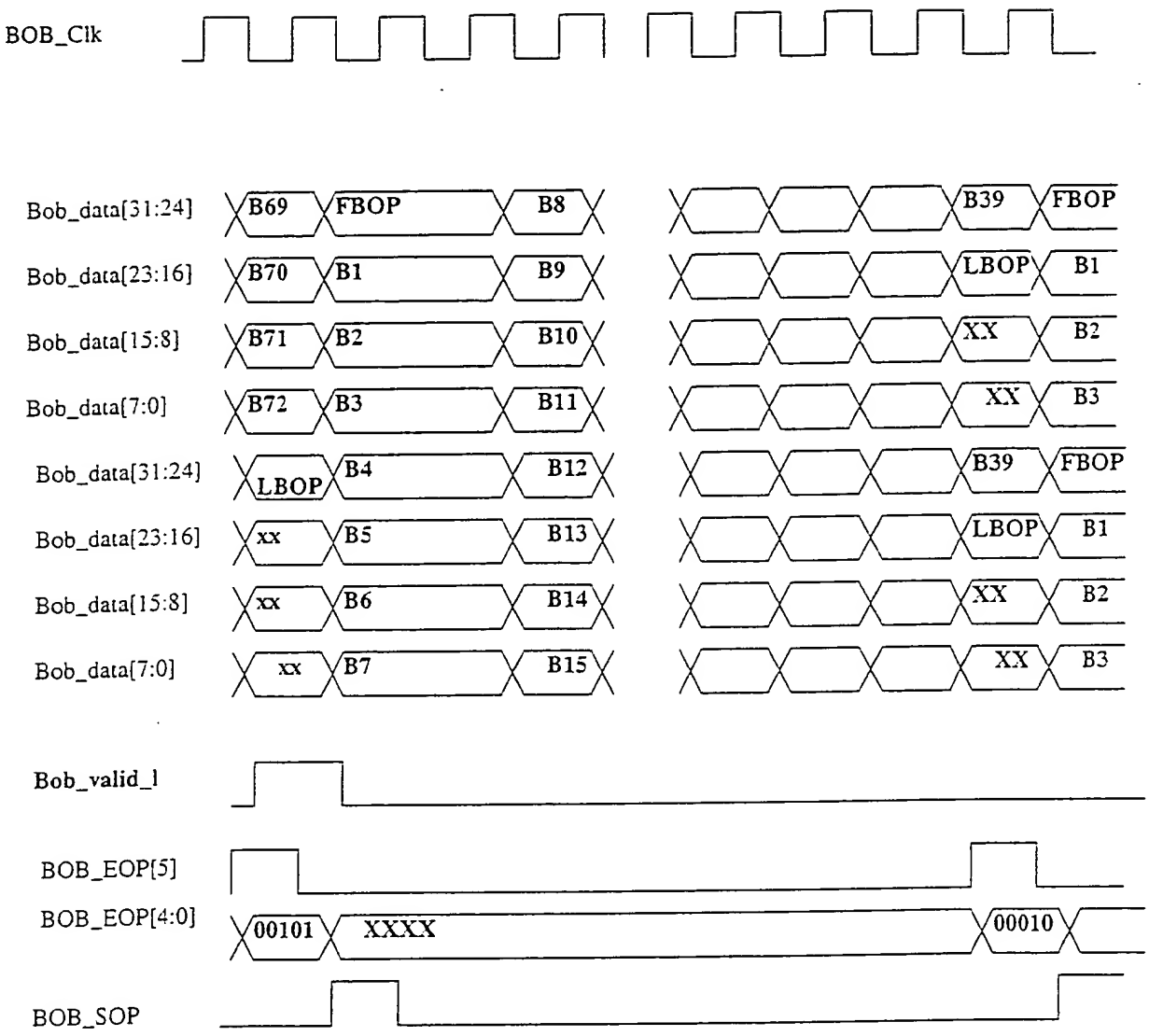


FIGURE 9.



Note: LBOP = Last Byte of Packet, FBOP = First byte of packet.

FIGURE 10.

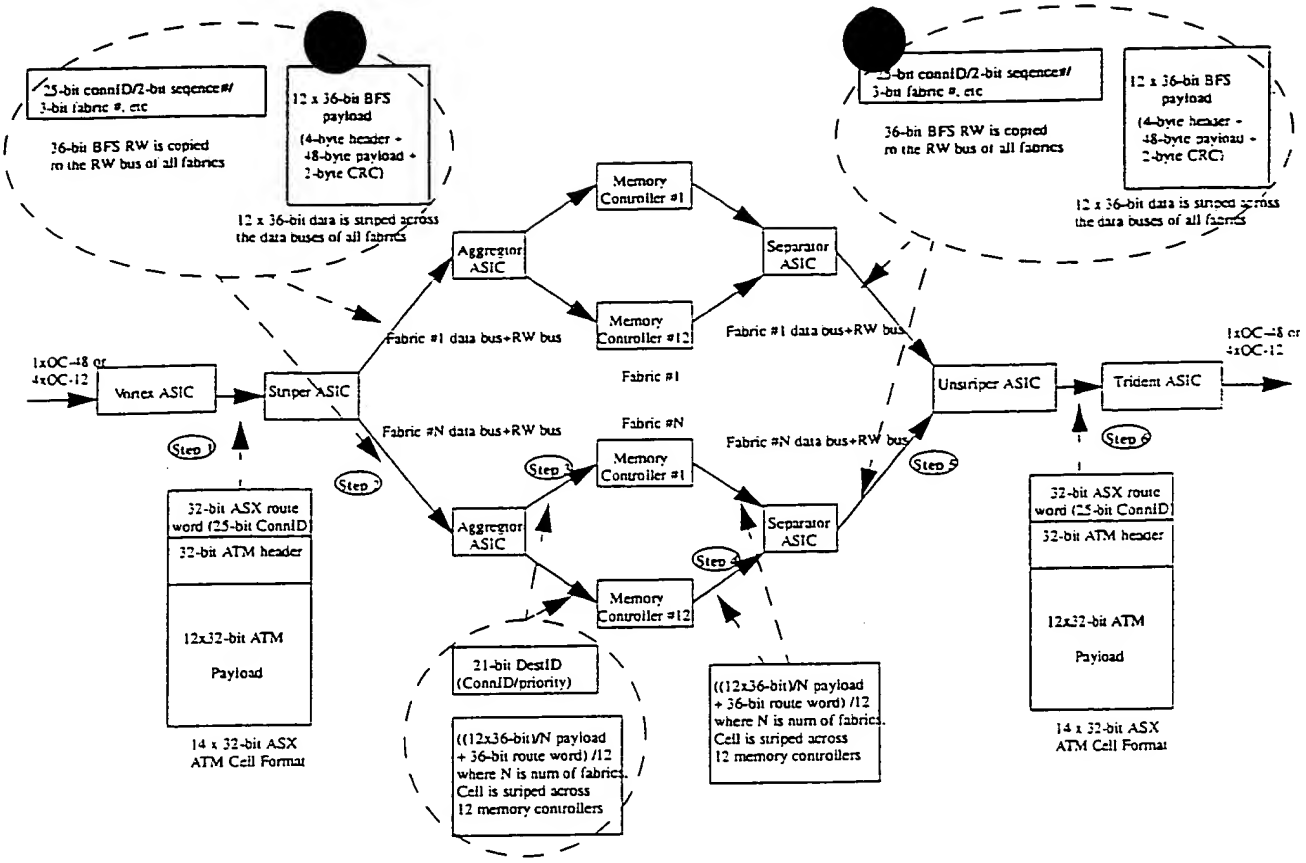


FIGURE 11.

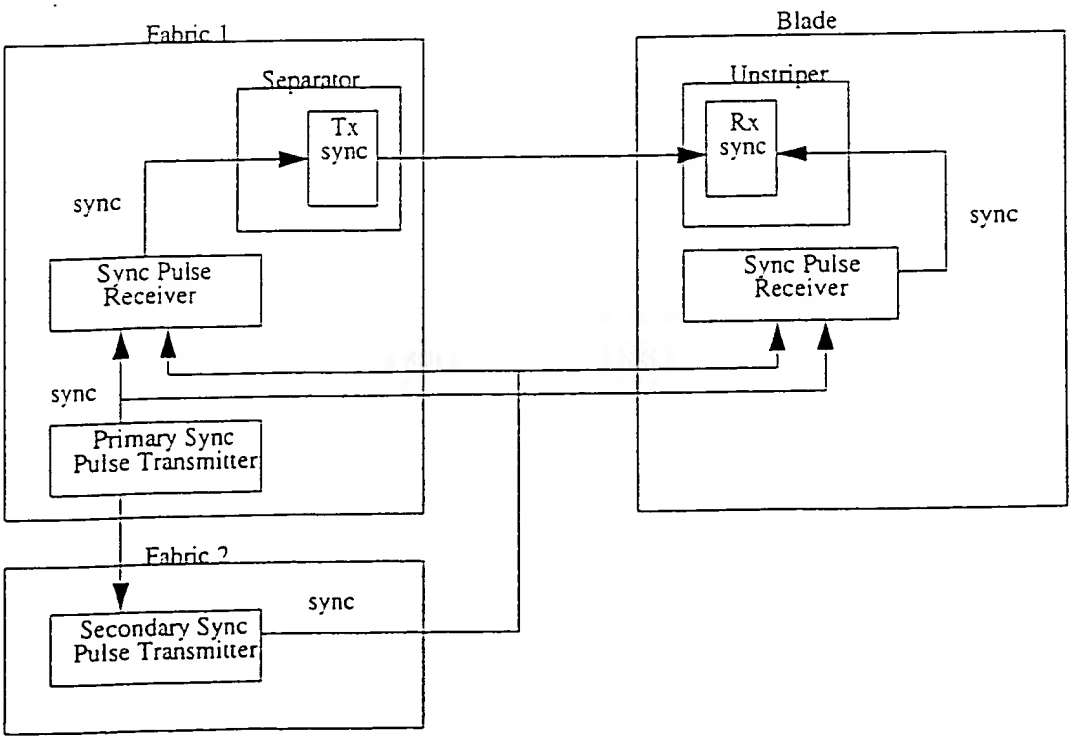


FIGURE 12.

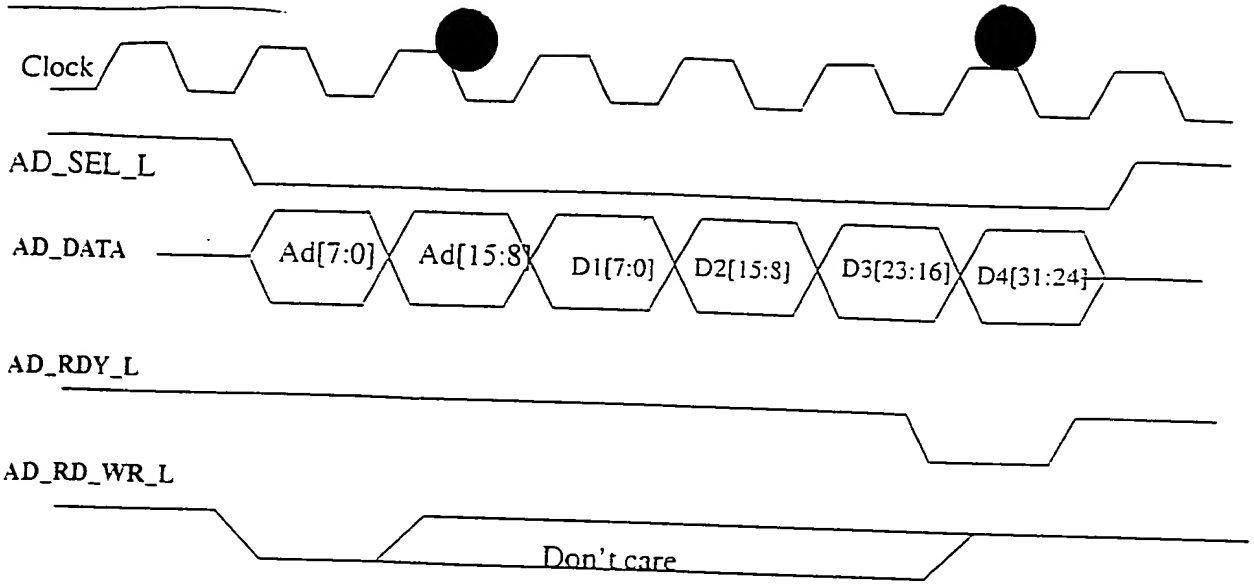


FIGURE 13.

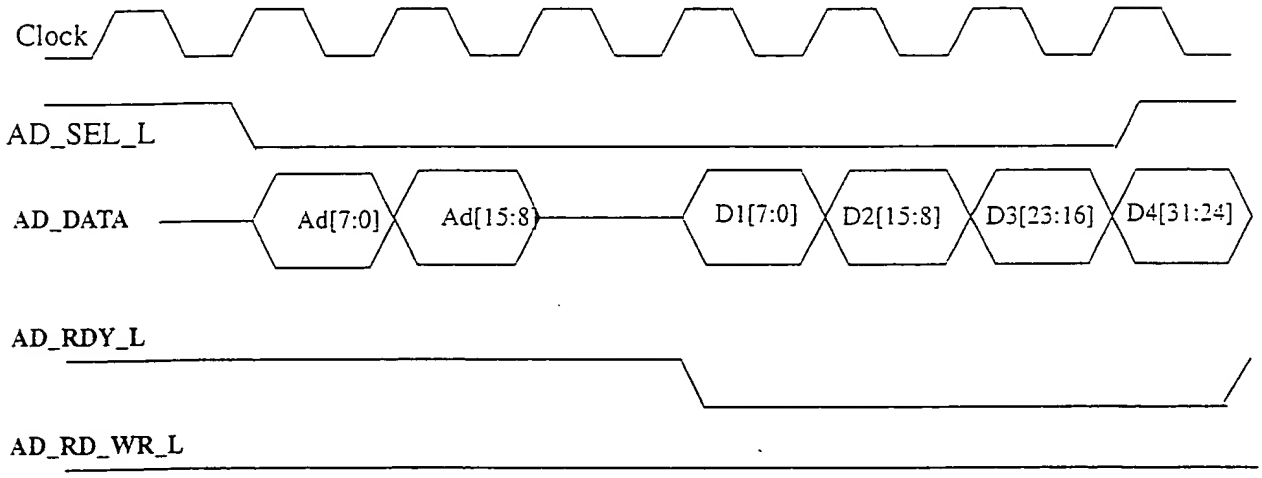


FIGURE 14.

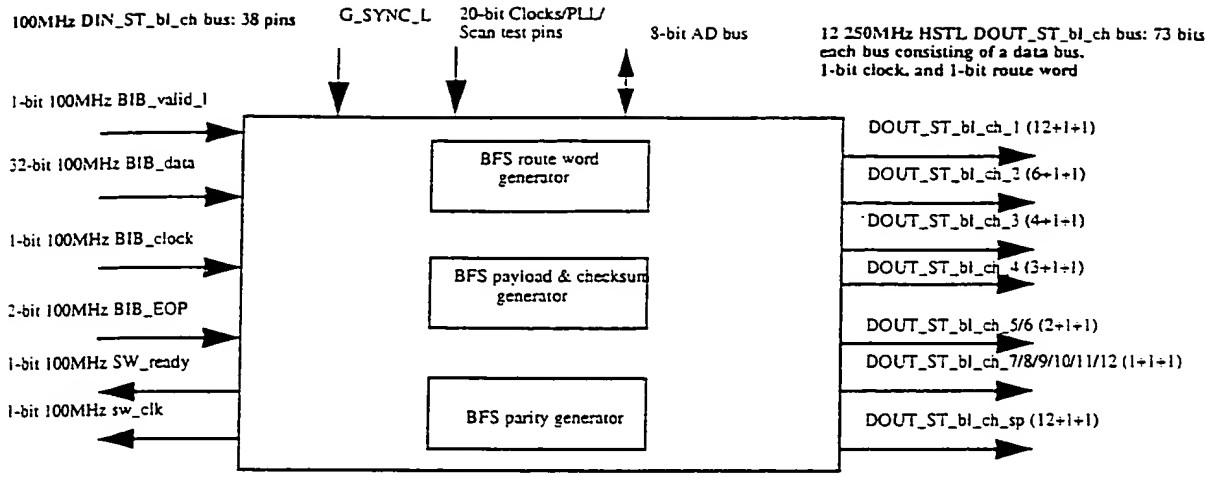


FIGURE 15.

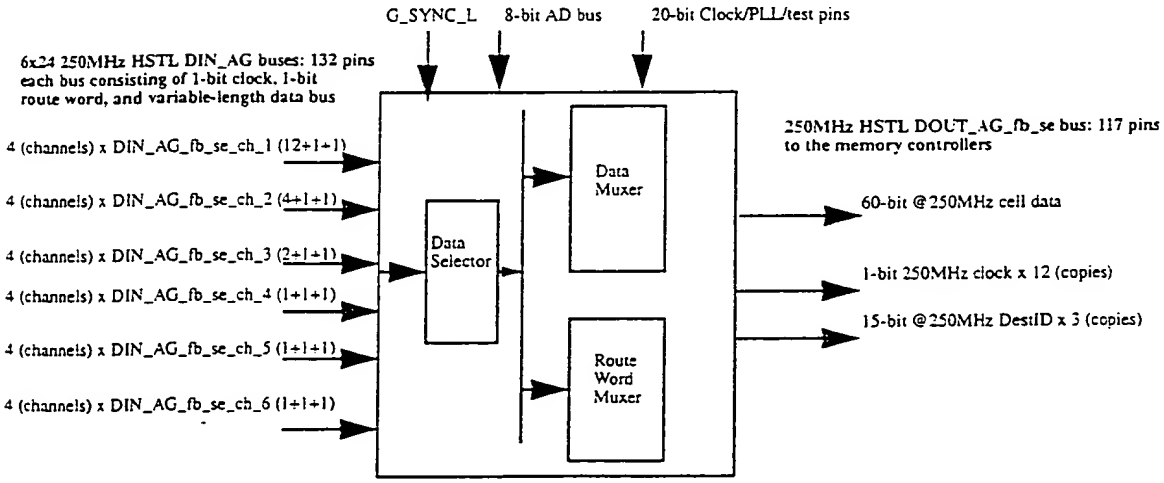


FIGURE 16.

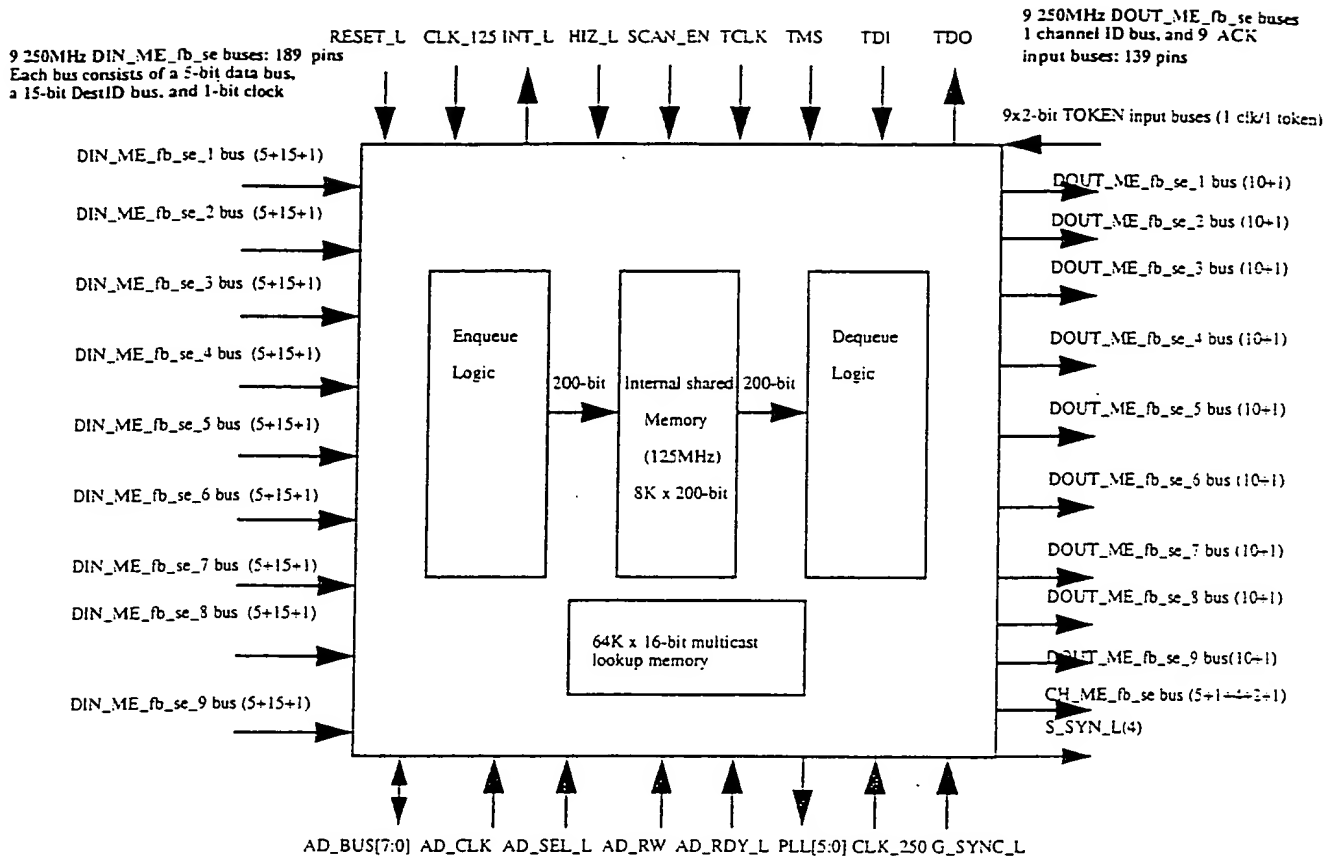


FIGURE 17.

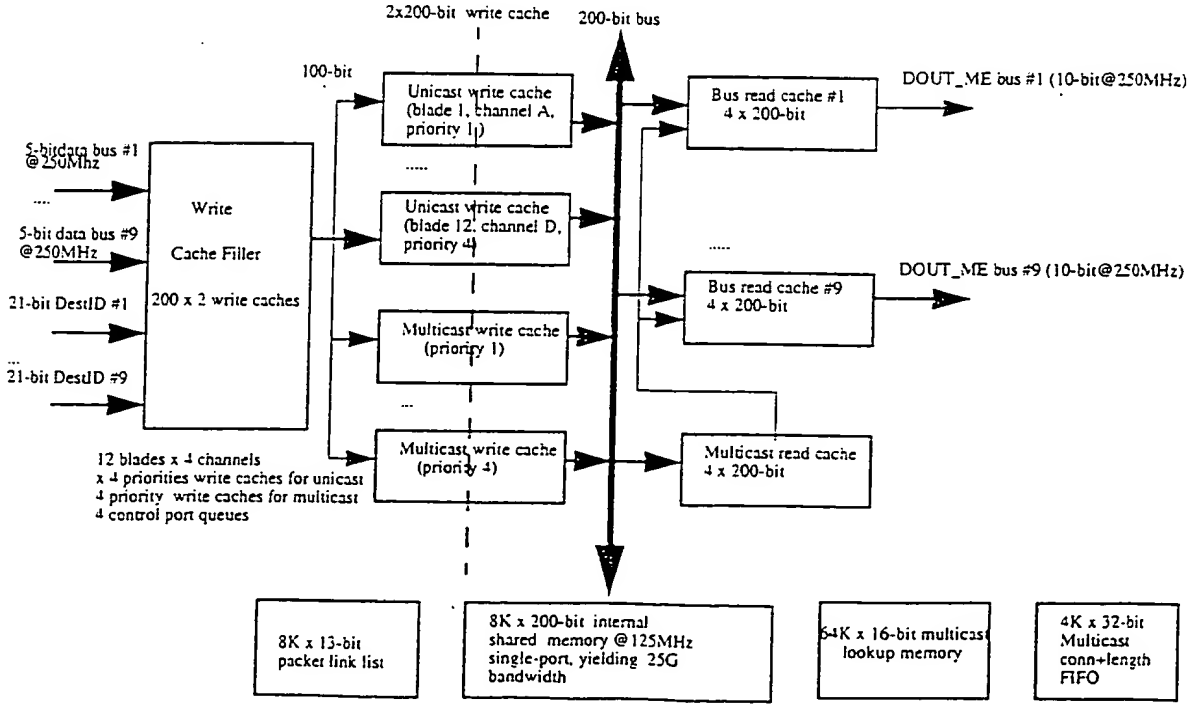


FIGURE 18.

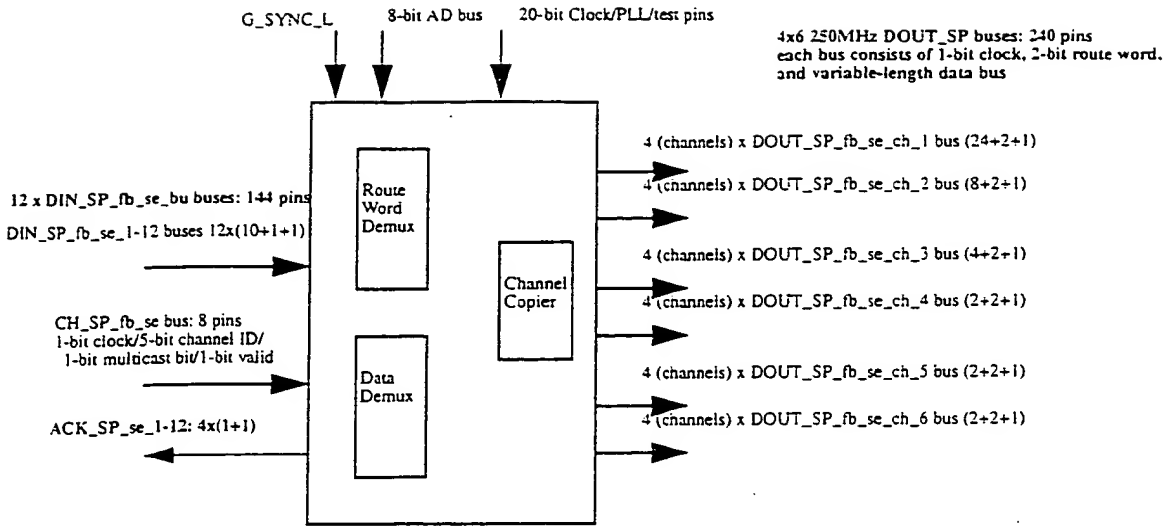
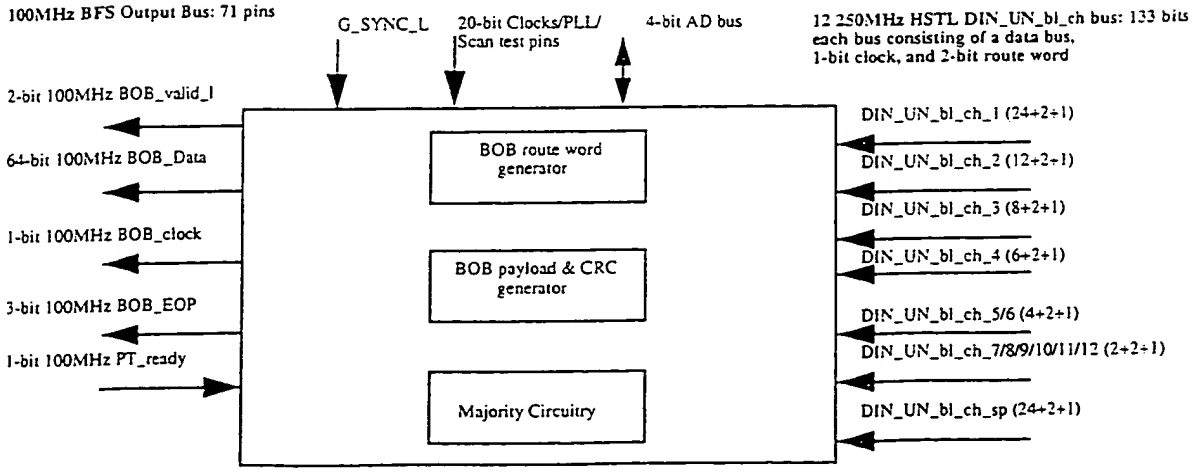
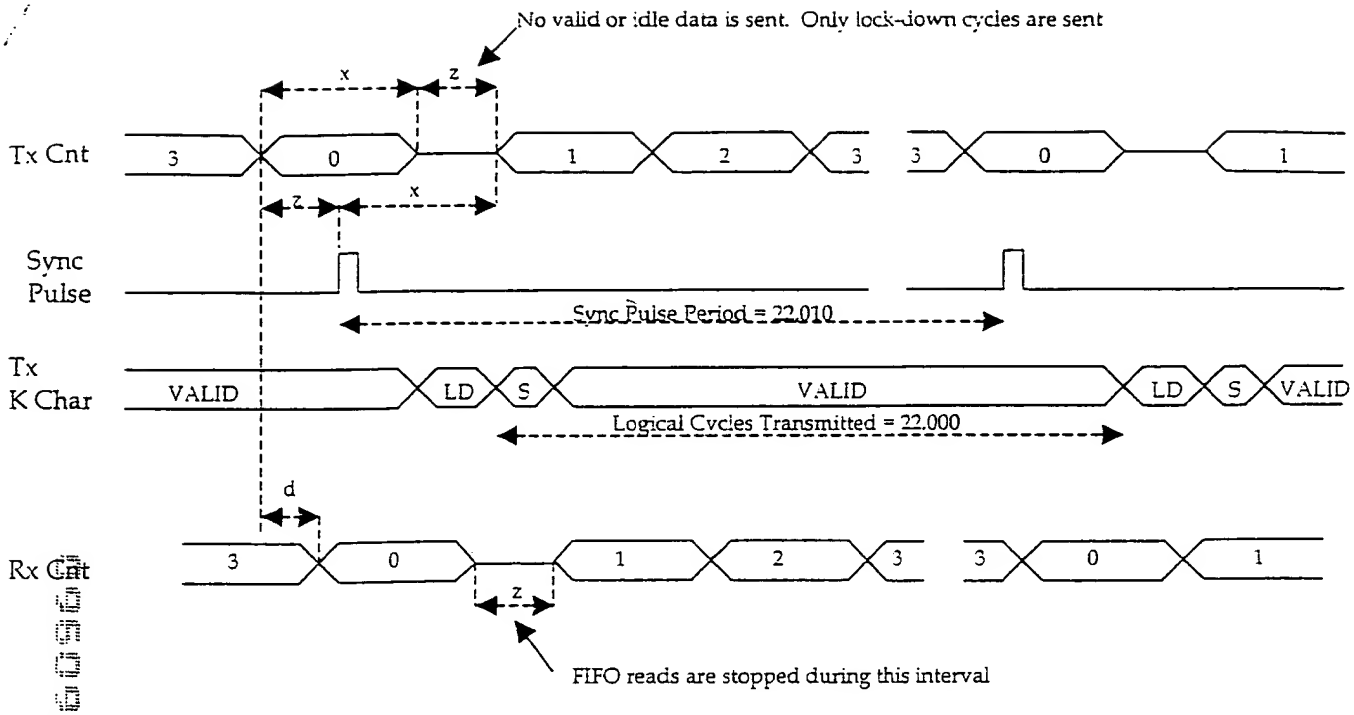


FIGURE 19.





d = Sync Pulse Receive Delay = 150
x = Counter Tick Length = 250
z = Lock-down Amount

Figure 20

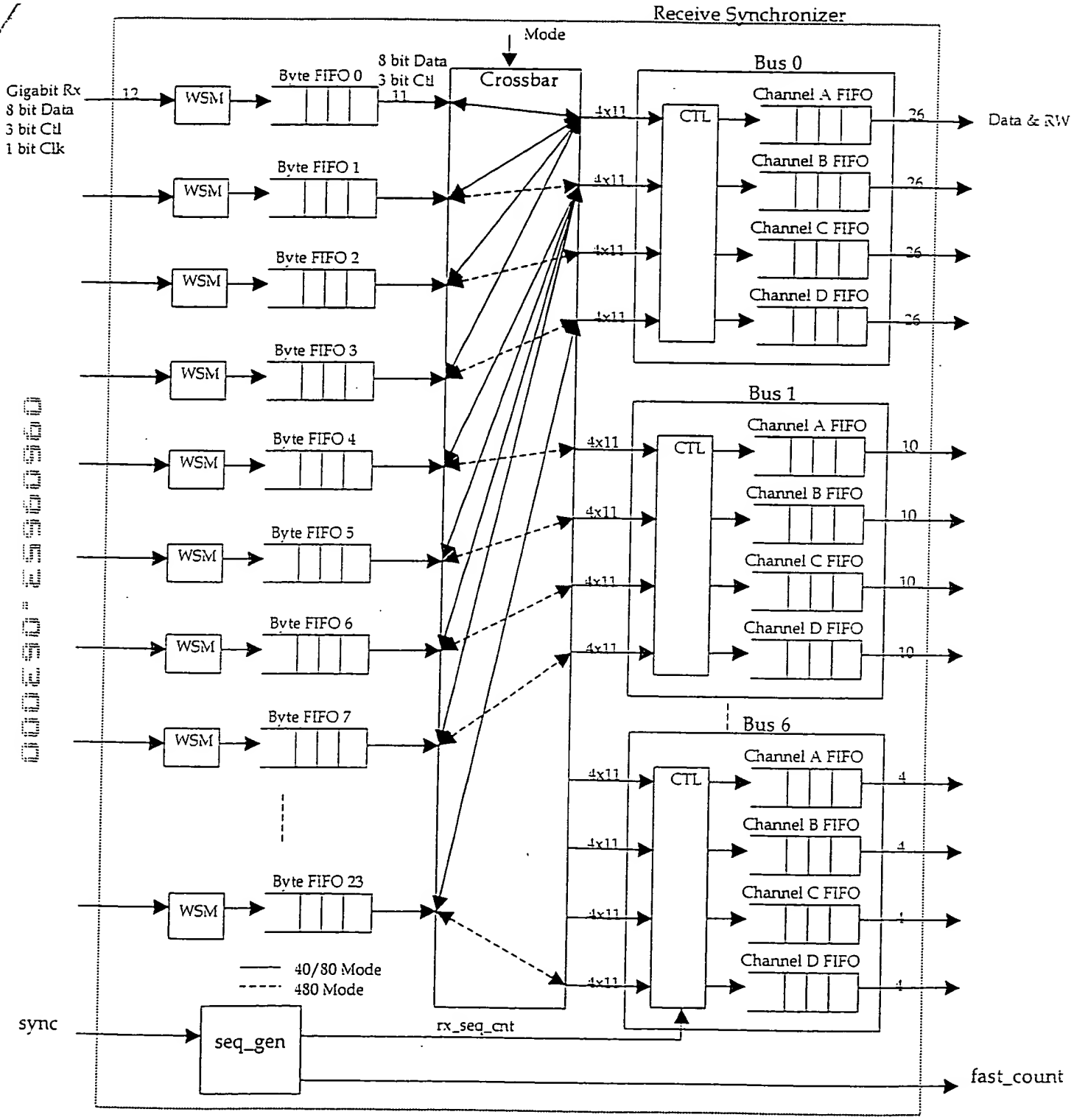

Receive Synchronizer
Mode
Crossbar
8 bit Data
3 bit Ctl
11
Gigabit Rx
8 bit Data
3 bit Ctl
1 bit Clk
12
WSM
Byte FIFO 0
Byte FIFO 1
Byte FIFO 2
Byte FIFO 3
Byte FIFO 4
Byte FIFO 5
Byte FIFO 6
Byte FIFO 7
Byte FIFO 23
Bus 0
Bus 1
Bus 6
CTL
4x11
Channel A FIFO
Channel B FIFO
Channel C FIFO
Channel D FIFO
26
10
4
Data & RW
40/80 Mode
480 Mode
sync
seq_gen
rx_seq_cnt
fast_count

Figure 21

000290 "E5960960

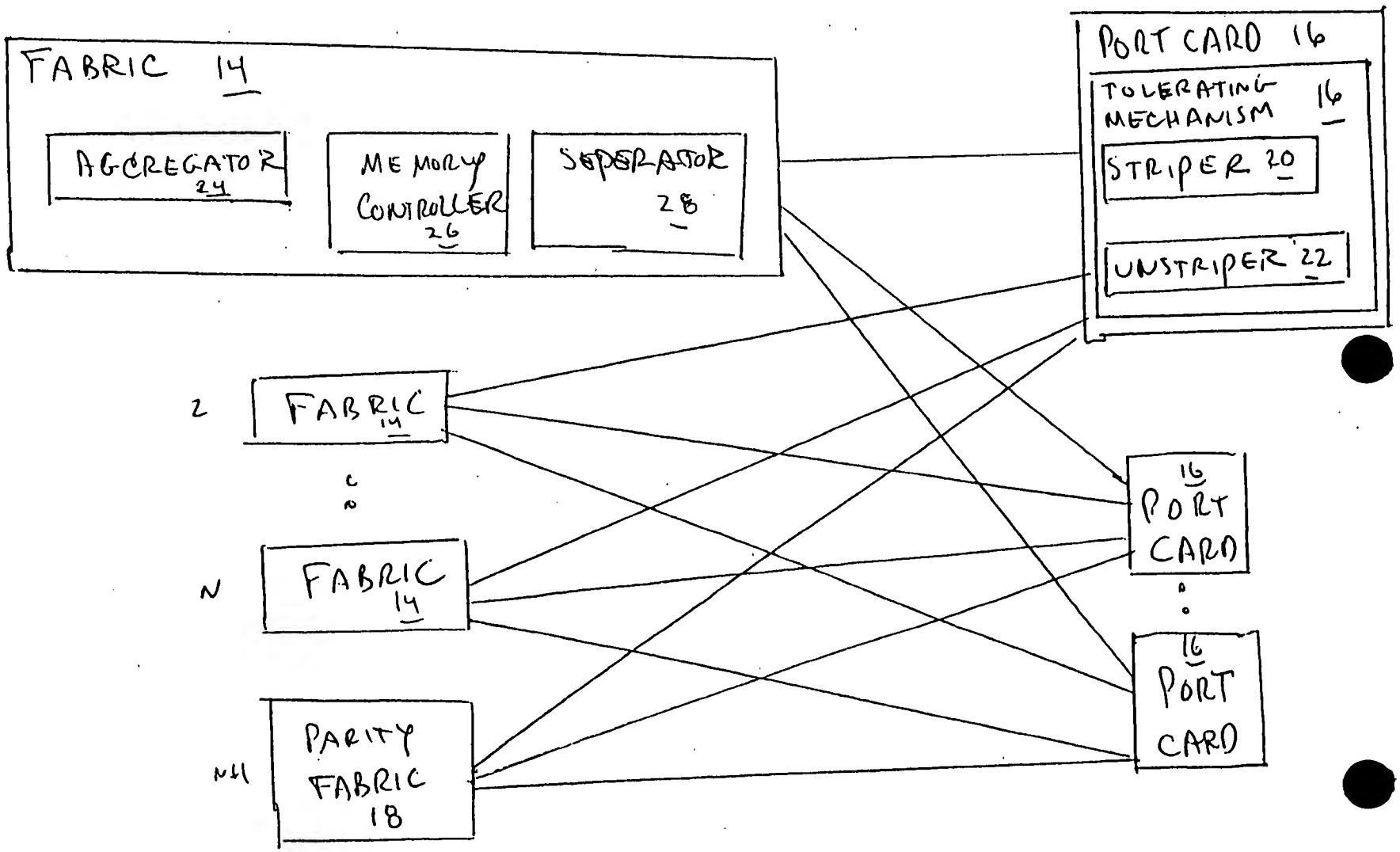


FIG 22